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CIRCUIT ARRANGEMENT FOR GENERATING SQUARE PULSES

Prior Art

The invention relates to a circuit arrangement for generating square pulses, having an edge-triggered flip-flop and at least one comparator, whose output is connected to the trigger input of the flip-flop, and an energy-storing element, which is charged in alternation as a function of the switching state of the flip-flop, and at least one switching threshold resistor is connected in series with the energy-storing element, at which resistor a voltage generated by the current flowing through the energy-storing element drops, which voltage is fed to the signal input of the comparator.

Using such a circuit arrangement to generate square pulses is known. The known circuit is used for instance to measure the field intensity of a magnetic field. A magnetic field probe, which is embodied as an inductive resistor and represents the energy-storing element, is placed in the magnetic field to be measured. The magnetic field probe is embodied such that it is brought to saturation by the magnetic field to be measured and the magnetic field generated by the current. As long no external magnetic field acts on the magnetic field probe, or in other words the magnetic field to be measured is zero, the magnetic field probe has an electrical behavior that in terms of an electric current flowing through it is independent of the direction of the current. The result is a pulse-duty factor of 1:1 in the square pulses generated by the circuit arrangement.

If an external magnetic field acts on the magnetic field probe, that is, if the magnetic field to be measured is no longer zero, then magnetic field probe reaches saturation

earlier in one direction than in the other. With respect to
an electric current flowing through it, its behavior is
therefore no longer dependent on the direction of the
electric current. As a result, the pulse-duty factor of the
5 square pulses changes. Hence the pulse-duty factor of the
square pulses represents a measure of the magnetic field
acting on the magnetic field probe.

A known circuit arrangement is shown in Fig. 3. In the
known circuit arrangement, two comparators are provided,
10 whose outputs are fed to an AND gate 22, whose output is
connected to the trigger input of the flip-flop 21. The
signal inputs of the comparators are each connected to a
different end of the energy store, that is, of the magnetic
field probe. The energy store is connected between the two
15 outputs of the flip-flop. Thus depending on the position of
the flip-flop, current flows in a different direction through
the energy store. Between the outputs of the flip-flop and
the energy store, respective switching threshold resistors
are connected. The junction points of the switching
20 threshold resistors and the energy store are each connected
to the signal input of a comparator. The two reference
inputs of the comparators are connected to one another, so
that the same reference voltage is present at both
comparators.

At the output of the flip-flop at which there was no
output voltage, there is now an output voltage after a
switchover of the flip-flop, and there is no longer an output
voltage at the other output. By means of the energy stored
in the coil, the original current flow is, however,
30 maintained. As a result, the potential at the signal input
of the applicable comparator drops below the switching
threshold. As a consequence, the voltage at the output of

the applicable comparator becomes zero. As a result, the output of the AND gate also becomes zero, so that the voltage at the signal input of the applicable comparator again reaches the switching threshold, causing the comparator again to output an output signal, and the AND gate is switched through. By means of the edge occurring upon switching of the AND gate, the flip-flop is triggered again, so that it switches over once again, and the process just described is repeated. The circuit is dimensioned such that it oscillates at a frequency of approximately 350 kHz.

However, the known circuit has the disadvantage that the tolerances of the switching threshold resistors and the tolerances of the switching thresholds of the comparators affect the pulse-duty factor. Moreover, different delay times of the comparators adversely affect the symmetry of the circuit arrangement. The transit time of the AND gate also adversely affects the resolution achieved with the circuit. Furthermore, additional costs result from the use of an AND gate.

It is object of the invention to embody a circuit arrangement of the type defined at the outset in such a way that the influence of the tolerance in the components is lessened.

This object is attained by the characteristics of the body of claim 1. Advantageous refinements of the invention are defined by the dependent claims.

Advantages of the Invention

According to the invention, a circuit arrangement for generating square pulses, having an edge-triggered flip-flop

and at least one comparator, whose output is connected to the trigger input of the flip-flop, and an energy-storing element, which is charged in alternation as a function of the switching state of the flip-flop, and at least one switching
5 threshold resistor is connected in series with the energy-
storing element, at which resistor a voltage generated by the current flowing through the energy-storing element drops,
which voltage is fed to the signal input of the comparator,
is characterized in that the energy-storing element is
10 disposed in the transverse branch of a bridge, in each of the four bridge segments of which a respective switch is disposed, and the switches are each connected in pairs in crossover fashion by the flip-flop, so that the current flow in the transverse branch is reversible, and that the bridge
15 is connected in series with the switching threshold resistor, and the junction point of the bridge to the switching threshold resistor is connected to the signal input of the comparator.

By means of the arrangement according to the invention,
20 it is advantageously unnecessary to have two switching threshold resistors. Not only is the space required reduced, but by using only one switching threshold resistor, asymmetries are avoided. This is because the current flowing through the energy store always flows through the same
25 switching threshold resistor, and thus the tolerance of the switching threshold resistor has the same effect in both switching states of the flip-flop.

The same is true for a tolerance of the comparator.
Since only one comparator is used, tolerances in terms of the
30 switching threshold and the transit time in both switching states of the flip-flop have the same effect. Moreover, both space and expense are saved by using only one comparator.

Since in the circuit arrangement of the invention an AND gate is no longer necessary, the transit times caused by such an element cannot have any effect. Omitting the AND gate advantageously also saves space.

5 An embodiment of the invention in which the energy-storing element is an inductive resistor has proved especially advantageous. Because the energy-storing element is an inductive resistor, it can be embodied as a magnetic field probe, as provided in a further particular embodiment
10 of the invention. When the inductive resistor is embodied as a magnetic field probe, the circuit arrangement of the invention can especially advantageously be used for measuring a magnetic field.

15 It is especially advantageous if the magnetic field probe is used to detect the magnetic field of a core of a compensation current sensor, as is contemplated in a further particular embodiment of the invention. By using the circuit arrangement of the invention in a compensation current sensor, the accuracy of the compensation current sensor can
20 be improved in a simple way.

25 In an advantageous feature of the invention, the comparator is embodied as a digital gate. As a result, the circuit can be produced quite economically. An AND gate could for instance be used as the digital gate. However, it has also proved advantageous to use an analog comparator,
which as its output signals furnishes digital signals that are simple to process further.

30 In a further particular embodiment of the invention, it is advantageously provided that the switches are MOSFETs, of which two are triggered directly and two are triggered via

inverters from the outputs of the flip-flop. Embodying the switches as MOSFETs makes it simple to realize the circuit arrangement of the invention. Moreover, triggering of the switches is made simpler. In addition, the switches can be
5 triggered quite precisely.

It has furthermore been found that it is especially advantageous if in the transverse branch of the bridge, a series resistor is connected in series with the energy-storing element. By means of the series resistor, the
10 sensitivity of the circuit arrangement can be adjusted in a simple way.

Further details, characteristics and advantages of the present invention will become apparent from the ensuing description of a particular exemplary embodiment in
15 conjunction with the drawing.

Drawing

Shown are: Fig. 1, one embodiment of the circuit arrangement of the invention in a schematic diagram; Fig. 2, a compensation current sensor in a schematic diagram; and
20 Fig. 3, a circuit arrangement of the prior art in a schematic diagram.

Description of the Exemplary Embodiments

As can be seen from Fig. 1, a coil 3 and a series resistor 11 connected in series with it are disposed in the
25 transverse branch of a bridge. One switch 7, 8, 9, 10 embodied as a MOSFET is disposed in each of the four bridge segments of the bridge. The first switch 7, disposed in one part of the bridge, and the second switch 8 are embodied as

p-channel MOSFETs and connected to the operating voltage V_{cc} .
The third switch 9 disposed in the other part of the bridge
and the fourth switch 10 are embodied as n-channel MOSFETs
and connected to a switching threshold resistor 4. The other
5 end of the switching threshold resistor 4 is connected to
ground.

The junction point between the n-channel MOSFETs 9, 10
and the switching threshold resistor 4 is connected to the
signal input 2a of a comparator 2. A reference voltage U_{ref}
10 is applied to the reference input 2b of the comparator 2.
The output of the comparator 2 is connected to the trigger
input of an edge-triggered flip-flop 1. The non-inverting
output 1a of the flip-flop 1 is connected, via a first
inverter 5, to the gate of the first, p-channel MOSFET 7 as
15 well as being connected directly to the gate of the fourth,
n-channel MOSFET 10, disposed in crossover fashion in the
bridge. The inverting output 1b of the flip-flop 1 is
connected, via a second inverter 6, to the gate of the
second, p-channel MOSFET 8 as well as being connected
20 directly to the gate of the third, n-channel MOSFET 9,
disposed in crossover fashion in the bridge. The MOSFETs 7,
8, 9, 10 are thus connected through in pairs in crossover
fashion (7 and 10, on the one hand, and 8 and 9, on the
other) by the flip-flop 1.

25 If the output voltage is applied to the non-inverting
output 1a of the flip-flop 1, then the first MOSFET 7 and the
fourth MOSFET 10 are switched through. A current can thus
flow from left to right in the transverse branch of the
bridge, or in other words can flow through the series
30 resistor 11 and the inductive resistor 3.

If the flip-flop 1 switches over, then the output

voltage is applied to the inverting output 1b of the flip-flop 1. As a result, the first MOSFET 7 and the fourth MOSFET 10 block, while conversely the second MOSFET 8 and the third MOSFET 9 are switched through.

5 Because the current flow in Fig. 2 from left to right through the transverse branch of the bridge is maintained as a result of the storage action of the inductive resistor 3, the voltage at the signal input 2a of the inverter 2 drops below the value of the reference voltage U_{ref} located at the
10 reference input 2b of the comparator 2. As a result, the output signal of the comparator becomes zero.

15 Once the direction of the current in the transverse branch of the bridge has reversed, because of the switching through of the second MOSFET 8 and the third MOSFET 9, or in other words once the current is now flowing from right to left through the transverse branch of the bridge or in other words through the inductive resistor 3 and the series resistor 11, the voltage drop brought about at the switching threshold resistor 4 increases again. Once it has reached the switching threshold of the comparator 2, the output
20 voltage again prevails at the output of the comparator 2. By means of the edge created by the switchover of the comparator 2, the flip-flop 1 is triggered, so that it switches over once again. The switching cycle described above thus begins
25 all over again.

30 The comparator 2 is for instance an analog comparator, which at its output furnishes a digital signal, which be processed simply in the downstream digital circuit. As the comparator 2, it is for instance also possible to use a component designed as a digital gate, optionally with a suitably adapted circuit design.

In the circuit arrangement of a compensation current sensor, shown in Fig. 2, a field probe 12 is disposed in the air gap of an annular core 13 that surrounds a conductor 15 whose current is to be measured. The field probe 12 is embodied as an inductive resistor, which reaches saturation as a result of the magnetic field in the air gap of the annular core 13. The field probe 12 is the inductive resistor 3 included in Fig. 1, and it is disposed in a circuit arrangement of Fig. 1 that forms part of a controller 16. The pulse-duty factor, brought about by the magnetic field in the air gap of the annular core 13, of the square pulses generated by the circuit arrangement in Fig. 1 is evaluated in the controller 16 and used to generate a compensation current I_A . The compensation current I_A is conducted through a coil 14 that is wound around the annular core 13.

The controller 16 is designed such that the current flowing through the coil 14 is high enough that the magnetic field in the air gap of the annular core 13 is nearly zero. As a result, the current I_A flowing through the coil 14 can be used as a measure for the current flowing through the conductor 15. To generate an output voltage U_A , a resistor 17, at which the output voltage U_A drops, is connected in series with the coil 14.

In Fig. 3, the known circuit arrangement, discussed in the background section above, is shown. As can be seen from Fig. 3, an inductive resistor 25 is connected between the outputs of an edge-triggered flip-flop 21. Depending on the position of the flip-flop 21, current thus flows in a different direction through the inductive resistor 25. A respective switching threshold resistor 26, 27 is connected between the outputs of the flip-flop 21 and the inductive

resistor 25. The junction point of the switching threshold
resistors 26, 27 is connected to the signal input of a
respective comparator 23, 24. The two reference inputs of
the comparators 23, 24 are connected to one another, so that
5 the same reference voltage is present at both comparators.
The outputs of the comparators 23, 24 are connected to the
two inputs of an AND gate 22. The output signals of these
comparators thus affect the switching states of the AND gate.
10 The output of the AND gate 22 is connected to the trigger
input of the flip-flop 21.

For further information on the mode of operation of the
known circuit shown in Fig. 3, see the background section
above.